

Cadence Allegro Design Entry Hdl Reference Guide

Hardware Description Language Demystified Dr. Cherry Bhargava 2020-08-27 Get familiar and work with the basic and advanced Modeling types in Verilog HDL Key Features _ Learn about the step-wise process to use Verilog design tools such as Xilinx, Vivado, Cadence NC-SIM _ Explore the various types of HDL and its need _ Learn Verilog HDL modeling types using examples _ Learn advanced concept such as UDP, Switch level modeling _ Learn about FPGA based prototyping of the digital system Description Hardware Description Language (HDL) allows analysis and simulation of digital logic and circuits. The HDL is an integral part of the EDA (electronic design automation) tool for PLDs, microprocessors, and ASICs. So, HDL is used to describe a Digital System. The combinational and sequential logic circuits can be described easily using HDL. Verilog HDL, standardized as IEEE 1364, is a hardware description language used to model electronic systems. This book is a comprehensive guide about the digital system and its design using various VLSI design tools as well as Verilog HDL. The step-wise procedure to use various VLSI tools such as Xilinx, Vivado, Cadence NC-SIM, is covered in this book. It also explains the advanced concept such as User Define Primitives (UDP), switch level modeling, reconfigurable computing, etc. Finally, this book ends with FPGA based prototyping of the digital system. By the end of this book, you will understand everything related to digital system design. What will you learn _ Implement Adder, Subtractor, Adder-Cum-Subtractor using Verilog HDL _ Explore the various Modeling styles in Verilog HDL _ Implement Switch level modeling using Verilog HDL _ Get familiar with advanced modeling techniques in Verilog HDL _ Get to know more about FPGA based prototyping using Verilog HDL Who this book is for Anyone interested in Electronics and VLSI design and want to learn Digital System Design with Verilog HDL will find this book useful. IC developers can also use this book as a quick reference for Verilog HDL fundamentals & features. Table of Contents 1. An Introduction to VLSI Design Tools 2. Need of Hardware Description Language (HDL) 3. Logic Gate Implementation in Verilog HDL 4. Adder-Subtractor Implementation Using Verilog HDL 5. Multiplexer/Demultiplexer Implementation in Verilog HDL 6. Encoder/Decoder Implementation Using Verilog HDL 7. Magnitude Comparator Implementation Using Verilog HDL 8. Flip-Flop Implementation Using Verilog HDL 9. Shift Registers Implementation Using Verilog HDL 10. Counter Implementation Using Verilog HDL 11. Shift Register Counter Implementation Using Verilog HDL 12. Advanced Modeling Techniques 13. Switch Level Modeling 14. FPGA Prototyping in Verilog HDL

Design Tutorial and Comparative Analysis of Printed Circuit Board Production Softwares for Microcontroller & FPGA-based Systems Ryan Hoover 2009 Printed Circuit Board design and implementation is a research topic most any company or group manufacturing electronic devices must address. There are issues in every aspect of design, ranging from schematics to pad stacks to routing. An error in any one of these steps propagates through the remaining steps. There are many approaches and software packages for moving a design from schematics to a physical PCB. However, software packages can range from being free to costing thousands. This thesis describes the design process from start to nish of a multi-purpose FPGA-based printed circuit board in two competing software packages. It will detail issues encountered along the way, and how these issues were resolved. Commercially available products will be used throughout the process, including the Xilinx Spartan 3E FPGA. The di ering design processes in the software packages Cadence Allegro Design Entry HDL and Advanced Circuits PCB Artist will be detailed and explored in order to determine which characteristics of each package suites various types of hardware design. The goal of this project is to create a "reference manual" for students to use as a guide in choosing a software package and working through the PCB design process at the University of Delaware's Electrical and Computer Engineering Department. These two software packages are available to all students in the department to learn and design printed circuit boards.

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